824MSTR PAGE 1

1 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

2 ;

3 ; Author : ADI - Apps www.analog.com/MicroConverter

4 ;

5 ; Date : 7 March 2000

6 ;

7 ; File : 824mstr.asm

8 ;

9 ; Hardware : ADuC824 or ADuC816

10 ;

11 ; Include File : UARTIO.asm - serial I/O routines

12 ;

13 ; Description : Demonstrates an example master mode SPI interface.

14 ; Code is intended for use with companion code file

15 ; '824slave.asm' running on a second MicroConverter

16 ; chip. Chips must have SCLK, MOSI, MISO, & GND pins

17 ; connected together, and P3.5 pin on master must

18 ; connect to SS pin on slave.

19 ;

20 ; If using the ADuC824 or ADuC816 eval board, you can

21 ; simply connect the 10-pin SPI/I2C header directly

22 ; to that of the master board. However, you must

23 ; also ensure that LK10 ('SS master') is REMOVED on

24 ; the slave board, and INSERTED on the master board.

25 ;

26 ; Once hardware is connected, download code to both

27 ; master & slave devices ('824mstr' to the master,

28 ; '824slave' to the slave). Reset the slave first,

29 ; and then the master. The slave will sit with the

30 ; LED off until the master starts exchanging data

31 ; with it at which time its LED will start blinking

32 ; in sync (or 180°out of phase) with that of the

33 ; master. When first launched, both master and slave

34 ; are transmitting zeros repeatedly on the SPI port.

35 ; Pressing the INT0 button on either master or slave

36 ; increments the value it is transmitting. Received

37 ; SPI data is relayed out the UART and can be viewed

38 ; on any VT100 terminal or terminal emulator at

39 ; 9600baud/8bits/noparity/1stopbit. Characters sent

40 ; from the terminal to the MicroConverter will update

41 ; the value being transmitted by SPI.

42 ;

43 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

44

45 $MOD824 ; Use 8052 & ADuC824 predefined symbols

46

00B4 47 LED EQU P3.4 ; P3.4 drives red LED on eval board

00B5 48 SS EQU P3.5 ; P3.5 drives slave device's SS pin

49

50 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

51 ; DEFINE VARIABLES IN INTERNAL RAM

---- 52 DSEG

0060 53 ORG 0060h

0060 54 INPUT: DS 1 ; data byte received by SPI

0061 55 OUTPUT: DS 1 ; data byte to send by SPI

56

57 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

58 ; BEGINNING OF CODE

824MSTR PAGE 2

---- 59 CSEG

60

0000 61 ORG 0000h

0000 02004B 62 JMP MAIN ; jump to main program

63

64 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

65 ; INTERRUPT VECTOR SPACE

0003 66 ORG 0003h ; (.................... INT0 ISR)

67

0003 0561 68 INC OUTPUT

0005 32 69 RETI

70

003B 71 ORG 003Bh ; (.................... SPI ISR)

72

003B D2B5 73 SETB SS ; pull slave's SS pin high

003D 85F760 74 MOV INPUT,SPIDAT

0040 32 75 RETI

76

77 ;====================================================================

78 ; MAIN PROGRAM

004B 79 ORG 004Bh

80

004B 81 MAIN:

82

004B 758107 83 MOV SP,#007h

84

85 ; CONFIGURE UART...

86

004E 75CBFF 87 MOV RCAP2H,#0FFh ; configure UART for 9830baud

0051 75CAFB 88 MOV RCAP2L,#-5 ; (close enough to 9600baud)

0054 75CDFF 89 MOV TH2,#0FFh

0057 75CCFB 90 MOV TL2,#-5

005A 759852 91 MOV SCON,#01010010b

005D 75C834 92 MOV T2CON,#00110100b

93

94 ; CONFIGURE SPI...

95

0060 75F837 96 MOV SPICON,#037h ; configure SPI port for:

97 ; Fosc/64, CPHA=1, CPOL=0, master

0063 75A901 98 MOV IEIP2,#1 ; enable SPI interrupt

99

100 ; CONFIGURE INTERRUPT 0...

101

0066 D288 102 SETB IT0 ; INT0 edge triggered

0068 D2A8 103 SETB EX0 ; enable INT0 interrupt

104

105 ; ENABLE INTERRUPTS & ENTER MAIN LOOP...

106

006A 756100 107 MOV OUTPUT,#0 ; set initial value for output byte

006D D2AF 108 SETB EA ; enable inturrupts

109

006F B2B4 110 LOOP: CPL LED ; flash the LED on the eval board

0071 E561 111 MOV A,OUTPUT ; byte to send via SPI into ACC

0073 12008E 112 CALL SENDSPI ; trigger SPI send/receive transfer

0076 120094 113 CALL DELAY ; pause 100ms

0079 E560 114 MOV A,INPUT ; send value received by SPI..

007B 1200C8 115 CALL SENDVAL ; ..out the UART as 2 ASCII chars

007E 900143 116 MOV DPTR,#SEPERATOR ; send line-feed & crdg-return..

824MSTR PAGE 3

0081 1200A8 117 CALL SENDSTRING ; ..out the UART

0084 3098E8 118 JNB RI,LOOP ; repeat (unless UART data received)

119

120 ; WHEN UART DATA RECEIVED, MOVE DATA TO SPI OUTPUT...

121

0087 859961 122 MOV OUTPUT,SBUF ; update OUTPUT byte to new value

008A C298 123 CLR RI ; must clear RI

008C 80E1 124 JMP LOOP ; back to main loop

125

126 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

127 ; SUBROUTINES

128

008E 129 SENDSPI: ; sends the value in ACC out the SPI port. also

130 ; receives simultaneously into SPIDAT. SPI interrupt

131 ; is triggered when transfer is complete.

132

008E C2B5 133 CLR SS ; must pull slave's SS pin low first

0090 8561F7 134 MOV SPIDAT,OUTPUT ; trigger data transfer

0093 22 135 RET

136

137 ; - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - -

138

0094 139 DELAY: ; delays approximately 100ms

140

0094 C0E0 141 PUSH ACC

0096 C0F0 142 PUSH B

0098 74C8 143 MOV A,#200 ; 100 \* 1ms = 100ms

009A 75F041 144 DLY1: MOV B,#65 ; 65 \* 15.26us = 1ms

009D D5F0FD 145 DJNZ B,$ ; sit here for 1ms

00A0 D5E0F7 146 DJNZ ACC,DLY1 ; repeat 100 times (100ms delay)

00A3 D0F0 147 POP B

00A5 D0E0 148 POP ACC

00A7 22 149 RET

150

151 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

152 ; SUBROUTINE INCLUDE FILE

153

=1 154 $INCLUDE(UARTIO.asm)

=1 155 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

=1 156 ;

=1 157 ; Author : ADI - Apps www.analog.com/MicroConverter

=1 158 ;

=1 159 ; Date : 12 October 1999

=1 160 ;

=1 161 ; File : UARTIO.hex

=1 162 ;

=1 163 ; Hardware : any 8051 based microcontroller or MicroConverter

=1 164 ;

=1 165 ; Description : standard UART I/O subroutines. total size of this

=1 166 ; code when assembled is 155 bytes. routines for use

=1 167 ; external to this file are:

=1 168 ;

=1 169 ; SENDSTRING - sends a string of characters

=1 170 ; SENDCHAR - sends a single character

=1 171 ; SENDVAL - sends a byte as 2 ASCII characters

=1 172 ; HEX2ASCII - converts from HEX to ASCII

=1 173 ; ASCII2HEX - converts from ASCII to HEX

=1 174 ; GETCHAR - gets a single character

824MSTR PAGE 4

=1 175 ; GETVAL - gets a byte as 2 ASCII characters

=1 176 ;

=1 177 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

=1 178

=1 179 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 180 ; SENDSTRING

=1 181

00A8 =1 182 SENDSTRING: ; sends ASCII string to UART starting at location

=1 183 ; DPTR and ending with a null (0) value

=1 184

00A8 C0E0 =1 185 PUSH ACC

00AA C0F0 =1 186 PUSH B

00AC E4 =1 187 CLR A

00AD F5F0 =1 188 MOV B,A

00AF E5F0 =1 189 IO0010: MOV A,B

00B1 05F0 =1 190 INC B

00B3 93 =1 191 MOVC A,@A+DPTR

00B4 6005 =1 192 JZ IO0020

00B6 1200C0 =1 193 CALL SENDCHAR

00B9 80F4 =1 194 JMP IO0010

00BB D0F0 =1 195 IO0020: POP B

00BD D0E0 =1 196 POP ACC

=1 197

00BF 22 =1 198 RET

=1 199

=1 200 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 201 ; SENDCHAR

=1 202

00C0 =1 203 SENDCHAR: ; sends ASCII value contained in A to UART

=1 204

00C0 3099FD =1 205 JNB TI,$ ; wait til present char gone

00C3 C299 =1 206 CLR TI ; must clear TI

00C5 F599 =1 207 MOV SBUF,A

=1 208

00C7 22 =1 209 RET

=1 210

=1 211 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 212 ; SENDVAL

=1 213

00C8 =1 214 SENDVAL: ; converts the hex value of A into two ASCII chars,

=1 215 ; and then spits these two characters up the UART.

=1 216 ; does not change the value of A.

=1 217

00C8 C0E0 =1 218 PUSH ACC

00CA C4 =1 219 SWAP A

00CB 1200DC =1 220 CALL HEX2ASCII

00CE 11C0 =1 221 CALL SENDCHAR ; send high nibble

00D0 D0E0 =1 222 POP ACC

00D2 C0E0 =1 223 PUSH ACC

00D4 1200DC =1 224 CALL HEX2ASCII

00D7 11C0 =1 225 CALL SENDCHAR ; send low nibble

00D9 D0E0 =1 226 POP ACC

=1 227

00DB 22 =1 228 RET

=1 229

=1 230 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 231 ; HEX2ASCII

=1 232

824MSTR PAGE 5

00DC =1 233 HEX2ASCII: ; converts A into the hex character representing the

=1 234 ; value of A's least significant nibble

=1 235

00DC 540F =1 236 ANL A,#00Fh

00DE B40A00 =1 237 CJNE A,#00Ah,$+3

00E1 4002 =1 238 JC IO0030

00E3 2407 =1 239 ADD A,#007h

00E5 2430 =1 240 IO0030: ADD A,#'0'

=1 241

00E7 22 =1 242 RET

=1 243

=1 244 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 245 ; ASCII2HEX

=1 246

00E8 =1 247 ASCII2HEX: ; converts A from an ASCII digit ('0'-'9' or 'A'-'F')

=1 248 ; into the corresponding number (0-15). returns C=1

=1 249 ; when input is other than an ASCII digit,

=1 250 ; indicating invalid output (returned as 255).

=1 251

00E8 C3 =1 252 CLR C

00E9 9430 =1 253 SUBB A,#'0'

00EB B40A00 =1 254 CJNE A,#10,$+3

00EE 401B =1 255 JC IO0050 ; if '0'<=char<='9', return OK

00F0 B41100 =1 256 CJNE A,#17,$+3

00F3 4013 =1 257 JC IO0040 ; if '9'<char<'A', return FAIL

00F5 9407 =1 258 SUBB A,#7

00F7 B41000 =1 259 CJNE A,#10h,$+3

00FA 400F =1 260 JC IO0050 ; if 'A'<=char<='F', return OK

00FC B42A00 =1 261 CJNE A,#42,$+3

00FF 4007 =1 262 JC IO0040 ; if 'F'<char<'a', return FAIL

0101 9420 =1 263 SUBB A,#20h

0103 B41000 =1 264 CJNE A,#10h,$+3

0106 4003 =1 265 JC IO0050 ; if 'a'<=char<='f', return OK..

=1 266

0108 C3 =1 267 IO0040: CLR C ; ..else return FAIL

0109 74FF =1 268 MOV A,#0FFh

=1 269

010B B3 =1 270 IO0050: CPL C

010C 22 =1 271 RET

=1 272

=1 273 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 274 ; GETCHAR

=1 275

010D =1 276 GETCHAR: ; waits for a single ASCII character to be received

=1 277 ; by the UART. places this character into A.

=1 278

010D 3098FD =1 279 JNB RI,$

0110 E599 =1 280 MOV A,SBUF

0112 C298 =1 281 CLR RI

=1 282

0114 22 =1 283 RET

=1 284

=1 285 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 286 ; GETVAL

=1 287

0115 =1 288 GETVAL: ; waits for two ASCII hex digits to be received by

=1 289 ; the UART. returns the hex value in A.

=1 290

824MSTR PAGE 6

0115 C0F0 =1 291 PUSH B

0117 C000 =1 292 PUSH 0

0119 C298 =1 293 IO0060: CLR RI

011B 310D =1 294 CALL GETCHAR ; first nibble

011D F500 =1 295 MOV 0,A ; store received char

011F 11E8 =1 296 CALL ASCII2HEX

0121 40F6 =1 297 JC IO0060 ; if not '0' thru 'F', don't accept

0123 C4 =1 298 SWAP A ; swap nibbles

0124 F5F0 =1 299 MOV B,A ; store nibble in B

0126 E500 =1 300 MOV A,0 ; echo received char

0128 11C0 =1 301 CALL SENDCHAR

012A C298 =1 302 IO0070: CLR RI

012C 310D =1 303 CALL GETCHAR ; second nibble

012E F500 =1 304 MOV 0,A ; store received char

0130 11E8 =1 305 CALL ASCII2HEX

0132 40F6 =1 306 JC IO0070 ; if not '0' thru 'F', don't accept

0134 45F0 =1 307 ORL A,B ; combine nibbles

0136 F5F0 =1 308 MOV B,A ; store results in B

0138 E500 =1 309 MOV A,0 ; echo received char

013A 11C0 =1 310 CALL SENDCHAR

013C E5F0 =1 311 MOV A,B ; final result

013E D000 =1 312 POP 0

0140 D0F0 =1 313 POP B

=1 314

0142 22 =1 315 RET

=1 316

317

318 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

319 ; TEXT DATA TABLES

320

0143 0A0D00 321 SEPERATOR: DB 10,13,0

322

323 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

324

325 END

326

VERSION 1.2h ASSEMBLY COMPLETE, 0 ERRORS FOUND

824MSTR PAGE 7

ACC. . . . . . . . . . . . . . . D ADDR 00E0H PREDEFINED

ASCII2HEX. . . . . . . . . . . . C ADDR 00E8H

B. . . . . . . . . . . . . . . . D ADDR 00F0H PREDEFINED

DELAY. . . . . . . . . . . . . . C ADDR 0094H

DLY1 . . . . . . . . . . . . . . C ADDR 009AH

EA . . . . . . . . . . . . . . . B ADDR 00AFH PREDEFINED

EX0. . . . . . . . . . . . . . . B ADDR 00A8H PREDEFINED

GETCHAR. . . . . . . . . . . . . C ADDR 010DH

GETVAL . . . . . . . . . . . . . C ADDR 0115H NOT USED

HEX2ASCII. . . . . . . . . . . . C ADDR 00DCH

IEIP2. . . . . . . . . . . . . . D ADDR 00A9H PREDEFINED

INPUT. . . . . . . . . . . . . . D ADDR 0060H

IO0010 . . . . . . . . . . . . . C ADDR 00AFH

IO0020 . . . . . . . . . . . . . C ADDR 00BBH

IO0030 . . . . . . . . . . . . . C ADDR 00E5H

IO0040 . . . . . . . . . . . . . C ADDR 0108H

IO0050 . . . . . . . . . . . . . C ADDR 010BH

IO0060 . . . . . . . . . . . . . C ADDR 0119H

IO0070 . . . . . . . . . . . . . C ADDR 012AH

IT0. . . . . . . . . . . . . . . B ADDR 0088H PREDEFINED

LED. . . . . . . . . . . . . . . NUMB 00B4H

LOOP . . . . . . . . . . . . . . C ADDR 006FH

MAIN . . . . . . . . . . . . . . C ADDR 004BH

OUTPUT . . . . . . . . . . . . . D ADDR 0061H

P3 . . . . . . . . . . . . . . . D ADDR 00B0H PREDEFINED

RCAP2H . . . . . . . . . . . . . D ADDR 00CBH PREDEFINED

RCAP2L . . . . . . . . . . . . . D ADDR 00CAH PREDEFINED

RI . . . . . . . . . . . . . . . B ADDR 0098H PREDEFINED

SBUF . . . . . . . . . . . . . . D ADDR 0099H PREDEFINED

SCON . . . . . . . . . . . . . . D ADDR 0098H PREDEFINED

SENDCHAR . . . . . . . . . . . . C ADDR 00C0H

SENDSPI. . . . . . . . . . . . . C ADDR 008EH

SENDSTRING . . . . . . . . . . . C ADDR 00A8H

SENDVAL. . . . . . . . . . . . . C ADDR 00C8H

SEPERATOR. . . . . . . . . . . . C ADDR 0143H

SP . . . . . . . . . . . . . . . D ADDR 0081H PREDEFINED

SPICON . . . . . . . . . . . . . D ADDR 00F8H PREDEFINED

SPIDAT . . . . . . . . . . . . . D ADDR 00F7H PREDEFINED

SS . . . . . . . . . . . . . . . NUMB 00B5H

T2CON. . . . . . . . . . . . . . D ADDR 00C8H PREDEFINED

TH2. . . . . . . . . . . . . . . D ADDR 00CDH PREDEFINED

TI . . . . . . . . . . . . . . . B ADDR 0099H PREDEFINED

TL2. . . . . . . . . . . . . . . D ADDR 00CCH PREDEFINED